

IN THE CLAIMS

Claims 1-14 were pending. Claims 1 and 9 have been amended. Claims 2-3 and 10-11 have been canceled without prejudice. Claims 1, 4-9, and 12-14 remain pending. A complete list of claims is presented below with amendments marked up:

Current Listing of Claims

1. (Currently amended) An apparatus, comprising:
a request unit to request more than one fragment of isochronous data from a memory device, wherein the isochronous data is overlay data organized in the memory device as more than one plane of data; and
a timer to insert a delay time between consecutive fragment requests for fragments from different planes.
2. Canceled.
3. Canceled.
4. (Original) The apparatus of claim 1, wherein the isochronous data is overlay data organized in the memory device into a first, a second, and a third plane of overlay data.
5. (Original) The apparatus of claim 4, the request unit to request fragments from the memory device in the sequence of from the first plane, then from the second plane, then from the third plane, the request unit to then repeat the sequence.
6. (Original) The apparatus of claim 5, wherein the timer inserts a delay after requests for fragments from the first and second planes.

7. (Original) A method, comprising:
organizing overlay data stored in a memory device into more than one plane;
issuing multiple requests for overlay data fragments from the memory device
where consecutive requests for overlay data fragments are requests for data from different
planes; and

inserting a delay between requests for overlay data fragments.

8. (Original) A method, comprising:
organizing overlay data stored in a memory device into a first plane, a second
plane, and a third plane;
requesting an overlay data fragment from the first plane;
inserting a delay after requesting the overlay data fragment from the first plane;
requesting an overlay data fragment from the second plane;
inserting a delay after requesting the overlay data fragment from the second plane;
requesting an overlay data fragment from the third plane; and
inserting a delay after requesting the overlay data fragment from the third plane.

9. (Currently amended) A system, comprising:
a memory device; and
a system logic device coupled to the memory device, the system logic device
including an overlay data unit, the overlay data unit including
a request unit to request more than one fragment of overlay data from the
memory device, wherein the overlay data is organized in the
memory device as more than one plane of data, and
a timer to insert a delay time between consecutive fragment requests for
fragments from different planes.

10. Canceled.

11. Canceled.

12. (Original) The system of claim 9, wherein the overlay data is organized in the memory device into a first, a second, and a third plane of overlay data.

13. (Original) The system of claim 12, the request unit to request fragments from the memory device in the sequence of from the first plane, then from the second plane, then from the third plane, the request unit to then repeat the sequence.

14. (Original) The system of claim 13, wherein the timer inserts a delay after requests for fragments from the first and second planes.